

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L8	10	((first one A) near2 (group set cluster)) same ((second two B) near2 (group set cluster)) same ((out output out\$put result SO) near2 (pin pad port connection line tab)) same (SM SW select selection select\$4 switch matrix matrices) same ((test normal (non\$test) normal) near3 (mode status cycdle state))).clm.	US-PGPUB; USPAT	OR	ON	2007/07/17 16:53
L15	192	((first one A) near2 (group set cluster)) near5 (((out output out\$put result SO) near2 (pin pad port connection line tab)) signal) and ((second two B) near2 (group set cluster)) near5 (((out output out\$put result SO) near2 (pin pad port connection line tab)) signal) and (SM SW select selection select\$4 switch matrix matrices) with ((test normal (non\$test) normal) near3 (mode status cycdle state)))) and "714"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/17 17:09
L16	40	15 and ((less few fewer reduced) near3 (pin pad port connection line tab))	US-PGPUB; USPAT	OR	ON	2007/07/17 17:20
L18	44	((first one A) near2 (group set cluster)) near5 (((out output out\$put result SO) near2 (pin pad port connection line tab)) signal) same((second two B) near2 (group set cluster)) near5 (((out output out\$put result SO) near2 (pin pad port connection line tab)) signal) same (SM SW select selection select\$4 switch matrix matrices) with ((test normal (non\$test) normal) near3 (mode status cycdle state))))	US-PGPUB; USPAT	OR	ON	2007/07/17 17:11
L19	5	18 and ((less few fewer reduced) near3 (pin pad port connection line tab))	US-PGPUB; USPAT	OR	ON	2007/07/17 17:14
L21	41	15 and 714/724.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/17 17:20
L22	6	16 and 714/724.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/17 17:20

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S22 9	35	("3961254" "4348759" "4402055" "4404519" "4602210" "4703484" "4724376" "4746855" "4760330" "4835464" "4972413" "4989209" "5025205" "5315241" "5384533" "5392297" "5416919" "5432797" "5442643" "5457380" "5479652" "5481471" "5525971" "5553082" "5566186" "5640404" "5712858" "5973504" "6073259" "6097199" "6122762" "6128758" "6134685" "6865701" "6883129").pn.	US-PGPUB; USPAT	OR	ON	2007/07/13 19:22
S22 8	71	("3961254" "4348759" "4402055" "4404519" "4602210" "4703484" "4724376" "4746855" "4760330" "4835464" "4972413" "4989209" "5025205" "5315241" "5384533" "5392297" "5416919" "5432797" "5442643" "5457380" "5479652" "5481471" "5525971" "5553082" "5566186" "5640404" "5712858" "5973504" "6073259" "6097199" "6122762" "6128758" "6134685" "6865701" "6883129").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:22
S22 7	981	("3961254" "4348759" "4402055" "4404519" "4602210" "4703484" "4724376" "4746855" "4760330" "4835464" "4972413" "4989209" "5025205" "5315241" "5384533" "5392297" "5416919" "5432797" "5442643" "5457380" "5479652" "5481471" "5525971" "5553082" "5566186" "5640404" "5712858" "5973504" "6073259" "6097199" "6122762" "6128758" "6134685" "6865701" "6883129").pn"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:21
S22 6	84	(tester same (dut cut uut sut (under adj test)) same (switch adj (circuit unit device chip module matrix matric\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:17
S22 5	23	(tester with (dut cut uut sut (under adj test)) with (switch adj (circuit unit device chip module matrix matric\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:11

## EAST Search History

S22 1	212	(tester with (dur cut uut sut (under adj test)) with (switch (switch adj matrix matrix\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:10
S22 3	129	S222 not S220	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:08
S22 2	133	S221 and @ad<="20040319"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:07
S22 0	113	S219 and @ad<="20040319"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 19:07
S21 9	133	S214 and (("714"/\$.ccls.) ("324"/\$. ccls.) (365/201.ccls.))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:52
S21 4	216	((tester (test adj (device unit apparatus module circuit chip))) near3 (less few fewer reduced limited) near3 (pin pad port input))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:51
S21 8	9	S214 and S215 and S216 and S217	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:48
S21 7	145365	(switch\$4 adj (circuit unit device chip module matrix matrix\$3)) same (output out-put out\$put)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:47

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S21 6	703900	((first one A) near2 (group set)) and ((second two B) near2 (group set))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:47
S21 5	586761	(plurality number several multitude) near3 (pin pad port output)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/13 18:47
S19 8	77082	(switch\$4 adj (circuit unit device chip module matrix matrix\$3)) same (output out-put out\$put)	US-PGPUB; USPAT	OR	ON	2007/07/13 18:47
S19 2	538223	((first one A) near2 (group set)) and ((second two B) near2 (group set))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:42
S19 4	173	((tester (test adj (device unit apparatus module circuit chip))) near3 (less few fewer reduced limited) near3 (pin pad port input))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:41
S19 3	419392	(plurality number several multitude) near3 (pin pad port output)	US-PGPUB; USPAT	OR	ON	2007/07/13 18:41
S21 3	526	S193 and S206 and S207 and S198	US-PGPUB; USPAT	OR	ON	2007/07/13 18:28
S20 0	6711	S193 and S192 and S198	US-PGPUB; USPAT	OR	ON	2007/07/13 18:28
S21 1	4953	S201 and US "20050005210".pn.	US-PGPUB; USPAT	OR	ON	2007/07/13 18:27
S20 1	4952	S193 and S192 and S191	US-PGPUB; USPAT	OR	ON	2007/07/13 18:27
S21 0	365	S193 and S206 and S207 and S191	US-PGPUB; USPAT	OR	ON	2007/07/13 18:26
S20 7	88957	((first one A) near2 (group set)) with ((signal output out-put out\$put SO))) and (((second two B) near2 (group set)) with ((signal output out-put out\$put SO)))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:21
S20 2	75974	((first one A) near2 (group set)) with ((signal output out-put out\$put SO) (pin pad port output))) and (((second two B) near2 (group set)) with ((signal output out-put out\$put SO) (pin pad port output)))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:21
S20 3	2427	S193 and S202 and S191	US-PGPUB; USPAT	OR	ON	2007/07/13 18:18

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S20 6	14842	((first one A) near2 (group set)) with ((pin pad port))) and ((second two B) near2 (group set)) with ((pin pad port)))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:17
S16 4	13	S163 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 18:16
S19 1	56599	(switch\$4 adj (circuit unit device chip module matrix matrix\$3)) with (output out-put out\$put)	US-PGPUB; USPAT	OR	ON	2007/07/13 18:13
S19 9	9	S194 and S193 and S192 and S198	US-PGPUB; USPAT	OR	ON	2007/07/13 17:52
S19 7	41	S194 and S193 and S192	US-PGPUB; USPAT	OR	ON	2007/07/13 17:49
S18 9	55	S186 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:03
S18 0	85	S179 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:03
S18 8	6250	S187 same ((test adj mode) ((first one A) near2 cycle))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:02
S18 6	314	S185 same ((test adj mode) ((first one A) near2 cycle))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:02
S18 7	399477	(out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 (select\$4 SM (switch near2 (circuit module device apparatus chip matrix matrix\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:01
S18 5	29972	(out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 (SM (switch near2 (circuit module device apparatus chip matrix matrix\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:01
S18 2	4103	S181 and ((test adj mode) ((first one A) near2 cycle))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:00
S18 1	26631	(out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 ((SM and (switch near2 (matrix matrix\$3))) (switch near2 (circuit module device apparatus chip matrix matrix\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 17:00
S18 3	1774	S182 and (test\$3 verif\$3)	US-PGPUB; USPAT	OR	ON	2007/07/13 16:58
S17 9	1937	(out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 (SM (switch near2 (matrix matrix\$3))) and (switch near2 (matrix matrix\$3))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:48

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S17 8	113	S177 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:37
S17 7	2248	(SO out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 (SM (switch near2 (matrix matric\$3))) and (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:36
S17 6	291	S174 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:34
S17 4	7126	(SO out output out-put (out\$put) result (test adj (out-put output out\$put result SO))) near4 (SM (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:33
S17 5	4	S174 and (tester with ((less fewer low lower reduc\$3) near3 (pin pad port line contact)))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:21
S16 9	410	(DUT CUT UUT) with (SM (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 16:13
S17 2	1	"6971045".pn.	USPAT	OR	ON	2007/07/13 16:12
S17 1	94	S170 same S165	US-PGPUB; USPAT	OR	ON	2007/07/13 14:49
S17 0	484	(DUT CUT UUT SUT (under near2 test)) with (SM (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 14:48
S16 3	59	(DUT CUT UUT) with (SO output out-put (out\$out) result) with (SM (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 14:47
S16 8	499	S165 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/13 13:58
S16 5	13097	(SO output out-put (out\$out) result) with (SM (switch near2 (matrix matric\$3)))	US-PGPUB; USPAT	OR	ON	2007/07/13 13:56

Continuing Data is mentioned in the first paragraph of the Specification. Continuing Data may be an insert from an Amendment to the Specification or may be present in the ADS.	Ok
If biological sequences are present in the application, the CRFE is present.	Ok
There are no missing or duplicate pages.	Ok
There is no illegible text as a result of scanning.	Ok
<b>Abstract</b>	<b>Status</b>
Abstract consists of no more than 25 lines.	Ok
Abstract is a single paragraph.	Ok
Abstract is on a separate page.	Ok
If no Abstract is present, the document is a 371 application.	Ok
<b>Amendments</b>	<b>Status</b>
After Final Amendment with Examiner initials and notation: "OK to Enter" or "Do not Enter". (Initials not required for Request for Reconsideration.)	Deficient
Examiners Amendment fees (such as Extension of Time fee or Additional Claim fee) are charged.	Ok
Examiners Amendments are signed by Primary Examiner or SPE and entered in PALM.	Ok
<b>Oath/Declaration</b>	<b>Status</b>
A Declaration (with the standard declaration statement) or a notarized Oath is present.	Ok
Certified copy of the Foreign Priority document is present.	Ok
Examiner has checked and initialed the Foreign Data boxes on the Bib Data Sheet.	Ok
Examiner has indicated acknowledgement of Foreign Priority on at least one Office Action.	Ok
Foreign Priority claimed is not more than 1 year (6 months for Design apps) from the effective filing date, if not claiming benefit of a PCT application.	Ok
Foreign Priority on the Bib Data Sheet agrees with the Oath/Declaration or the ADS.	Ok
Oath or Declaration (whichever is present) is signed by all applicants.	Ok
Residence, citizenship, and Post Office address are included for all applicants.	Ok
The following phrases are present: ...'the original and first inventors' ...'reviewed and understand' ...'acknowledge the duty to'.	Ok
<b>PCT Data</b>	<b>Status</b>
For applications filed under 35 USC 371, PCT Form 903 is present.	Ok
<b>USPTO Forms 892 and 1449</b>	<b>Status</b>
Application number is present and correct on each form.	Ok
Examiner has initialed or lined through each entry on all USPTO Forms 1449 or SB/08.	Ok
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entered the date at the bottom of USPTO Forms 1449 or SB08.	
Foreign Patent entries include publication number, country, and date (with month and year).	Ok
Non-Patent Literature (NPL) entries include a date (with at least the year).	Ok
US Patent entries include patent number, first-named inventor, and date (with month and year).	Ok
<b>Title (Design applications only)</b> 	<b>Status</b>
Title of the application matches the Title in the Claims.	Ok

**Text description for this application:**

You need to do the first sheet of the after final amendment